

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	10489450	native (NMOS or transistor or CMOS or MOS or device or element)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 16:18
L2	473	native adj (NMOS or transistor or CMOS or MOS or device or element)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 17:00
L3	13255	native SAME (NMOS or transistor or CMOS or MOS or device or element)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 17:02
L4	8	composite adj NMOS	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 17:01
L5	877	native near (NMOS or transistor or CMOS or MOS or device or element)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 17:02
L6	599016	bias	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 17:02
L7	245	5 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 17:02
L8	190	6 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 17:02

L9	55	7 not 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 17:20
L10	4833013	(zero adj threshold) or (low adj threshold)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 17:23
L11	386481	10 and (NMOS or transistor or CMOS or MOS)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 17:25
L12	386481	10 and 11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 17:26
L13	99203	12 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 17:27
L14	1522	13 and native	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 17:31